

FAST - (Untitled1:1)

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  - L6: (2579) L5 and second adj clock
  - L7: (152) S4 and (clock or docking) near3 error
  - L8: (5301818) L7 and dead time
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  - S3: (42166) amplifier and clock and counter
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US PATENT OFFICE

Default operator OR

S4 and (clock or docking) near3 error

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Doc ID	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Class	Inventor	SR	P	SP	SI	SA
1	US 6823025 B2	20041123	35	Amplitude modulation detector	375/320			Lee, Jaung-in et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	US 6811534 B2	20041102	32	Ambulatory medical apparatus and method using a telemetry system with predefined reception	600/300	604/131		Bowman, IV; Sam W. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	US 6803970 B1	20041012	16	Digital television receiver with match filter responsive to field synchronization code	348/725			Umberg; Allen LeRoy et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	US 6801706 B1	20041005	42	Jitter correcting apparatus and method for video signals	386/12	386/20; 386/21		Jesuk; Lee	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	US 6765445 B2	20040720	34	Digitally-synthesized loop filter circuit particularly useful for a phase locked loop	331/17	331/11; 331/16; 331/18		Perrott; Michael H. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	US 6763883 B2	20040720	68	Method and apparatus for improved communication in a wellbore utilizing acoustic signals	166/250.17	166/255.1		Green; Robert R. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	US 6733446 B2	20040511	30	Ambulatory medical apparatus and method using a telemetry system with predefined reception	600/300	340/573.1		Label; Ronald J. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	US RE38482 E	20040330	29	Delay stage circuitry for a ring oscillator	375/357	327/154; 327/156; 327/162		Leung; Wingyu et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
9	US 6677791 B2	20040113	38	Clock generation circuit, control method of clock generation circuit, clock reproducing circuit, semiconductor memory device, and dynamic random access memory device	327/158	341/143		Okuda; Yuichi et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
10	US 6664908 B2	20031216	16	Synchronized pulse width modulator	341/143	341/155; 341/156		Sundquist; Robert S. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
11	US 6664840 B2	20031216	7	Offset calibration system and method for a high gain signal channel	327/307	327/362		Cahalane; Aidan J. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>